these signals are Special trigger request bits

1) Every thing on THIS page are hand wire and done for the TPC system ONLY, other subsystem can do the same if it apply to them

2) the DAC2 output is used to provide V threshold for the MWPC through C1STATUS P13-10 (the connection between P13-10 and the FPGA U10-151 had been cut so the FPGA i/o pin won't claim the DAC output)

3) the detector CLK 1 is used for MWPC calibration pulse, so the jumper had been put on U9 -pin 1 and pin 8

NOTES: